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Application Information

Title Line One:: SUBSTRATE FOR SEMICONDUCTOR DEVICE,
Title Line Two:: SEMICONDUCTOR CHIP MOUNTING
Title Line Three:: SUBSTRATE, SEMICONDUCTOR DEVICE AND
Title Line Four:: METHOD OF FABRICATION THEREOF, AND

Title Line Five:: CIRCUIT BOARD, TOGETHER WITH
Title Line Six:: ELECTRONIC EQUIPMENT
Total Drawing Sheets:: 8
Docket Number:: 106364

Continuity Information

>This application is a::
Application One::
Filing Date::
Patent Number::
which is a::
>>Application Two::
Filing Date::
Patent Number::

Prior Foreign Applications

Foreign Application One:: 11-213184
Filing Date:: July 28, 1999
Country:: Japan
Priority Claimed:: yes
Foreign Application Two:: 2000-173294
Filing Date:: June 9, 2000
Country:: Japan
Priority Claimed:: yes
Foreign Application Three::
Filing Date::
Country::
Priority Claimed::